

Recent IBM Patents

The following patents were recently issued by the countries in which the inventions were made.

France		
7,201,484	J. L. Monrolin and J. M. Pierret	Translating Channel and Adaptive Equalizer
United Kingdom		
975,694	N. M. Leon	Surround for a Display
United States		
4,036,648	E. M. Engler, F. B. Kaufman and B. A. Scott	Highly Conductive Printing Medium Containing a Halogenated Hydrocarbon Photoactivator and a Tetrathiafulvalene or a Related Compound Thereof
4,037,971	J. G. Belleson and K. Clark	Inspection Tool
4,038,110	B.-C. Feng	Planarization of Integrated Circuit Surfaces Through Selective Photoresist Masking
4,038,439	R. M. Gibson and G. J. Sprokel	Method for Producing Selected Alignment in Liquid Crystal
4,038,567	S. C. Lewis, T. M. Redman, J. E. Rock and D. L. Wilder	Memory Input Signal Buffer Circuit
4,038,599	R. Bove and E. M. Hubacher	High Density Wafer Contacting and Test System
4,038,641	M. A. Bouknecht, D. G. Bourke and L. P. Vergari	Common Polling Logic for Input/Output Interrupt or Cycle Steal Data Transfer Requests
4,038,642	M. A. Bouknecht, M. I. Davis and L. P. Vergari	Input/Output Interface Logic for Concurrent Operations
4,038,645	R. E. Birney and M. L. Davis	Non-Translatable Storage Protection Control System
4,038,693	C. L. Huffine, L. F. Mattson, K. A. Shidler and N. E. Slindee	Anti-Static Magnetic Record Disk Assembly
4,039,338	W. R. Swiss and W. S. Young	Accelerated Sintering for a Green Ceramic Sheet
4,039,371	F. Brunner, P. Frasch and F. Schwerdt	Etchant for Polyimides
4,039,748	F. G. Caron, A. E. Desblache, D. N. Godard and F. P. Maddens	Method and Device for Synchronizing the Receiver Clock in a Data Transmission System
4,039,757	J. B. Frisone	Digital Data Communication Network and Control System Therefor
4,039,810	M. B. Heritage	Electron Projection Microfabrication System
4,039,856	E. S. Schlig	Distributed Josephson Junction Logic Circuit
4,039,861	L. G. Heller and D. P. Spampinato	Cross-Coupled Charge Transfer Sense Amplifier Circuits
4,039,867	R. J. Blumberg and J. A. Dorler	Current Switch Circuit Having an Active Load
4,039,936	A. L. Jones, J. P. Pawletko and J. W. Raider	Interleaved Magnetoresistive Displacement Transducers
4,039,978	L. G. Heller	Logic Controlled Charge Transfer Device Transversal Filter Employing Simple Weighting
4,040,016	H.-S. Lee and N. G. Vogl, Jr.	Twin Nodes Capacitance Memory
4,040,017	H.-S. Lee	Injected Charge Capacitor Memory
4,040,018	H. Chang, T. C. Chen and C. Tung	Ladder For Information Processing
4,040,020	D. M. Hannon and H. L. Hu	Bubble Lattice Device
4,040,034	L. A. Belady and C. J. Evangelisti	Data Security System Employing Automatic Time Stamping Mechanism
4,040,037	F. D. Lawlor	Buffer Chaining
4,040,038	L. L. Rosier	Column Accessing of Elements in Confined Arrays
4,040,105	N. E. Slindee	Magnetic Disk Drive Unit with Flexible Skirt
4,040,745	J. G. Belleson and K. Clark	Inspection Tool
4,040,748	J. G. Belleson and K. Clark	Inspection Tool
4,040,891	K. Chang and M. S. Pittler	Etching Process Utilizing the Same Positive Photoresist Layer for Two Etching Steps
4,040,895	W. J. Patrick and W. A. Westdorp	Control of Oxygen in Silicon Crystals
4,041,296	A. Dauby, V. Riso and D. Roger-Rodes	High-Speed Digital Multiply-By-Three Device
4,041,345	O. Sahni	Blue Color AC Gas Discharge Display Panel and Method
4,041,461	G. L. Kratz, W. W. Sproul III, E. T. Walendziewicz, D. E. Wallis and C. A. Dennis	Signal Analyzer System